



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani                      Art Unit : 1765  
Serial No.: 09,596,755                      Examiner : Robert Kunemund  
Filed : June 15, 2000  
Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND MANUFACTURING  
METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION


Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges  
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Total Claims	47	-	47	=	0	\$0
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Applicant hereby petitions under 37 C.F.R. §1.136 for a 2 month extension of time.						\$400
TOTAL FEE DUE						\$400

A check including \$400 is attached.

Respectfully submitted,

Date: 2/4/02

  
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